## Patent Abstracts of Japan
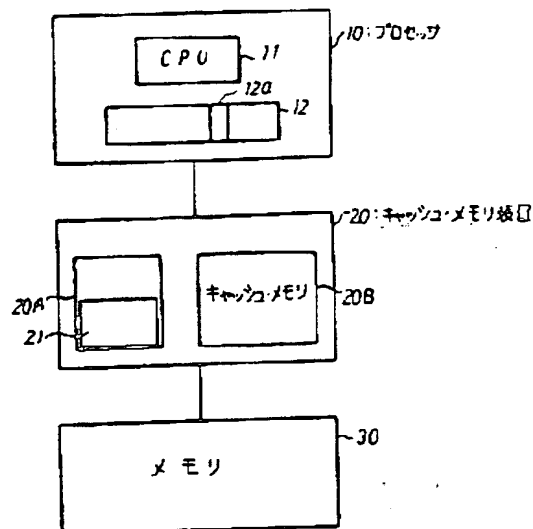
ABSTRACT  :  PURPOSE: To remove the hit rate reduction of a cache memory and to prevent the processing speed of original data processing from being dropped by freezing a cache by an entry replacing control means in the case of executing real time garbage collection.

CONSTITUTION: A processor 10 turns a real time garbage collection flag bit 12a of a status register 12 to a set state during the scanning processing of the real time garbage collection. When the bit 12a is set up, the entry replacing control means 21 in a cache memory device 20 freezes the cache. Thereby when a cache mishit is generated, a memory 30 is accessed without rewriting the entry of the cache. Consequently, the real time garbage collection is executed without reducing the hit rate of the cache memory during the period of original data processing and the processing speed of the original data processing is improved.

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